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APPLICANT: ISHII, KOJI

DOCKET NO: AUS920010940US1

SERIAL NO.: 09/975,202

FILE DATE: 10-11-2001

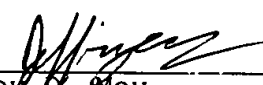
TITLE OF APPL.: METHOD AND APPARATUS FOR SHARING SIGNAL PINS  
ON AN INTERFACE BETWEEN A SYSTEM CONTROLLER  
AND PERIPHERAL INTEGRATED CIRCUITS

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

Weiss & Moy, P.C.  
4204 North Brown Avenue  
Scottsdale, AZ 85251-3989

April 30, 2002

I hereby certify that on the 30th day of April, 2002  
this correspondence is being deposited with the U.S.  
Postal Service as first class mail in an envelope  
addressed to: Assistant Commissioner for Patents,  
Washington, D.C. 20231.

  
Jeffrey D. Moy


**TYPE INITIAL SET**

Dear Sir:

Enclosed you will find four (4) sheets of formal drawings for  
the above-identified patent application.

Please charge any fees that may be incurred by this  
transmittal letter to our Deposit Account No. 23-0830.

Respectfully submitted,

  
Jeffrey D. Moy  
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JDM/sib  
Enclosures

00975202-050902